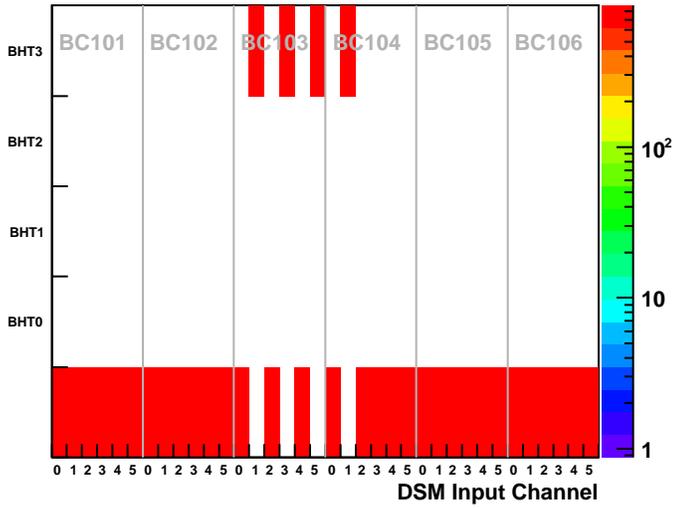


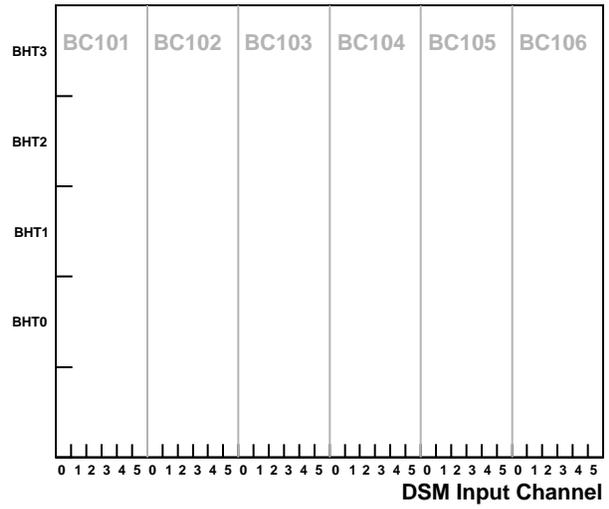
Barrel EMC L1 Input - High Tower Bits

Entries 31320



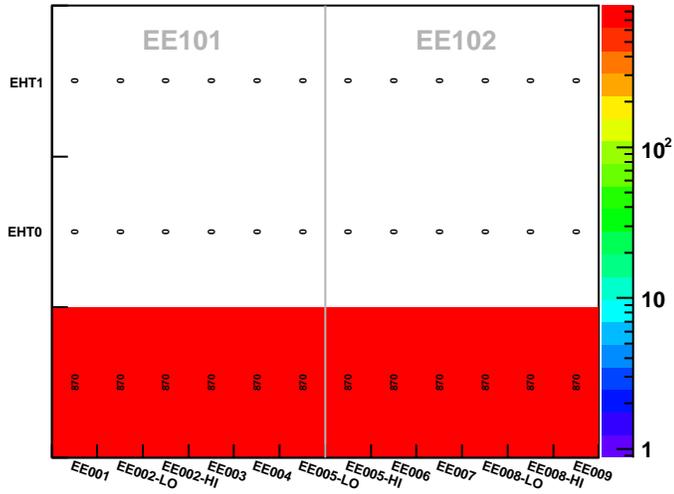
Barrel EMC L1 Input - High Tower Bits Errors

Entries 0



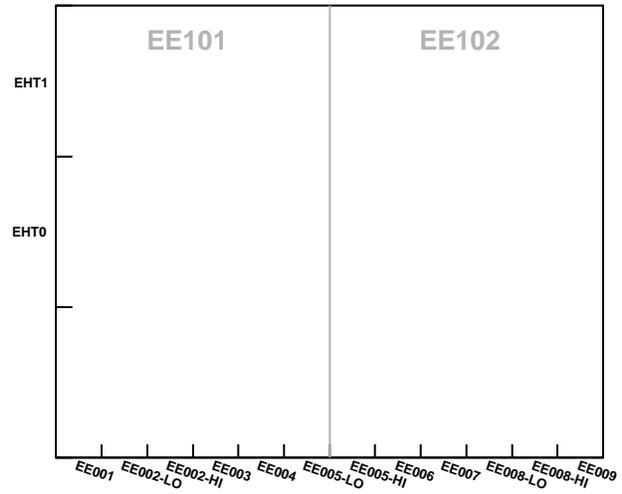
Endcap EMC L1 Input - High Tower Bits

Entries 10440



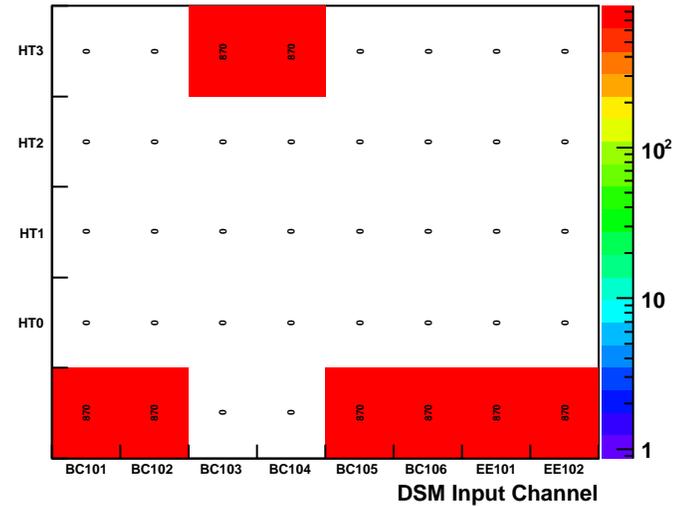
Endcap EMC L1 Input - High Tower Bits Errors

Entries 0



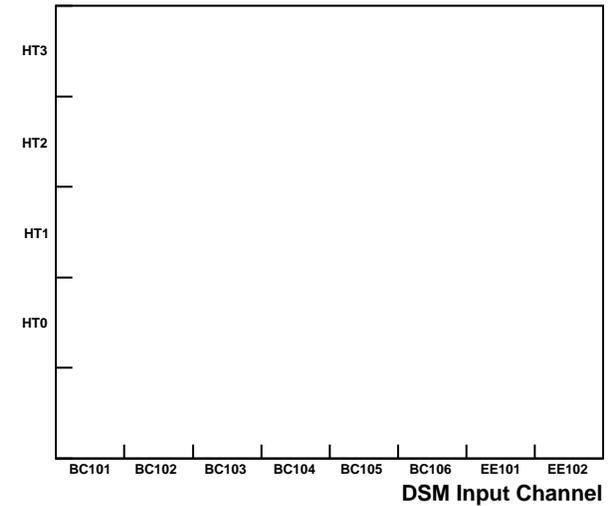
EMC L2 Input - High Tower Bits

Entries 6960



EMC L2 Input - High Tower Bits Errors

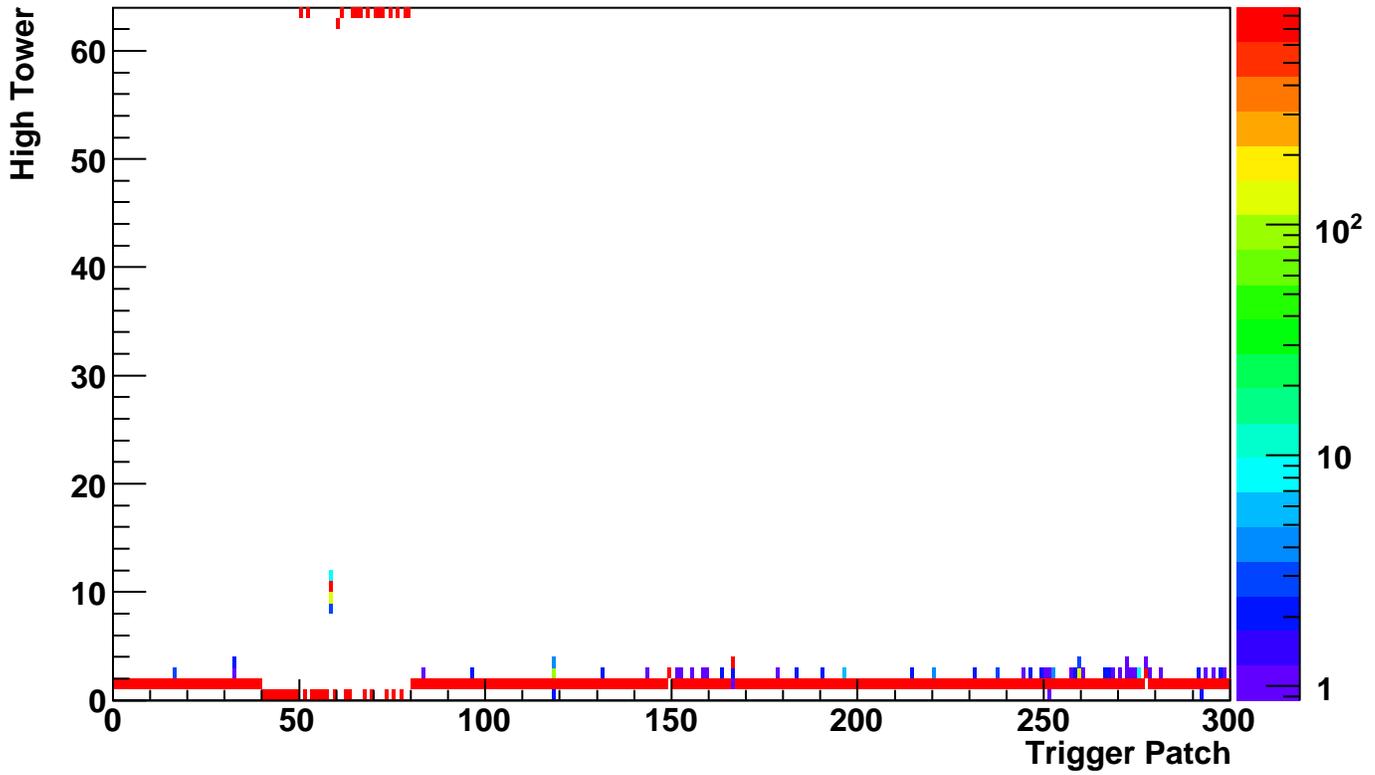
Entries 0





Barrel EMC L0 Input - High Tower

Entries 261000



Endcap EMC L0 Input - High Tower

Entries 78300

